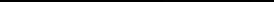




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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	10/666,482
Sheet	1	of	2	Filing Date	September 18, 2003
				First Named Inventor	Koji Yamada
				Art Unit	2883
				Examiner Name	ERIN CHIEM
				Attorney Docket Number	96790P442

U.S. PATENT DOCUMENTS

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Sheet	2	of	2		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T?
<i>eBc</i>		"Optical Interconnecting Structure of SI Waveguide on SOI Substrate"; T. Shoji, et al., NTT Telecommunications Energy Labs, one page (w/English abstract)	

**Examiner
Signature**

Ernst Siers

Date Considered

October 27, 2005

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